

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claim 52 in accordance with the following:

44. (PREVIOUSLY PRESENTED) A driving device of a plasma display panel having a plurality of first and second electrodes spaced apart from one another to form pairs and a plurality of cells formed between the first and second electrodes, and displaying according to the plural cells by applying drive voltages to said first and second electrodes, comprising:

- a first power supply having negative polarity;
- a second power supply having positive polarity;
- a scan driver connected to the plural first electrodes respectively;
- a first electrode common driver connected to the scan driver commonly; and
- a second electrode common driver connected to the plural second electrodes commonly;

wherein at a first timing for lightening the plural cells, drive current flows through a connection route of the second power supply, the first electrode common driver, the scan driver, the first electrode, the cell, the second electrode, the second electrode common driver and the first power supply, and

at a second timing for lightening the plural cells, drive current flows through a connection route of the second power supply, the second electrode common driver, the second electrode, the cell, the first electrode, the scan driver, the first electrode common driver and the first power supply.

45. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 44,

wherein driving operations at the first timing and the second timing are performed alternately, and

at transitions between the first and second timings, a connection route of the first power supply, the first electrode common driver, the scan driver, the first electrode, the cell, the second electrode, the second electrode common driver and the first power supply is formed so that the first power supply having negative polarity is commonly applied to the first and second electrodes.

46. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 44,

wherein driving operations at the first timing and the second timing are performed alternately, and

at transitions between the first and second timings, a connection route of the second power supply, the first electrode common driver, the scan driver, the first electrode, the cell, the second electrode, the second electrode common driver and the second power supply is formed so that the second power supply having positive polarity is commonly applied to the first and second electrodes.

47. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 44, further comprising:

a third power supply having potential between the first and second power supplies, wherein

driving operations at the first timing and the second timing are performed alternately, and

at transitions between the first and second timings, a connection route of the third power supply, the first electrode common driver, the scan driver, the first electrode, the cell, the second electrode, the second electrode common driver and the third power supply is formed so that the third power supply is commonly applied to the first and second electrodes.

48. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 47, wherein said third power supply has a voltage having negative polarity.

49. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 47, wherein said third power supply has a voltage having positive polarity.

50. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 47, further comprising:

a plurality of third electrodes crossing the plural first and second electrodes; and
an address driver connected to the plural third electrodes respectively,

wherein said address driver applies a ground potential to the plural third electrodes and maintains the plural third electrodes at the ground potential during the first and second timings.

51. (PREVIOUSLY PRESENTED) The driving device of a plasma display panel according to claim 47, wherein a large capacitance condenser is connected between terminals of the first power supply and the second power supply.

52. (CURRENTLY AMENDED) A driving device of plasma display panel according to claim 44, further comprising:

a plurality of third electrodes crossing the plural first and second electrodes and
an address driver connected to the plural third electrodes respectively,

wherein said address driver applies a ground potential to the plural third electrodes and maintains the plural third electrodes at the ~~group~~ground potential during the first and second timings.

53. (PREVIOUSLY PRESENTED) A driving device of plasma display panel according to claim 44, wherein, a large capacitance condenser is connected between terminals of the first power supply and the second power supply.